

### FEATURES

#### High Speed:

- 50 MHz Unity Gain Bandwidth
- 350 V/ $\mu$ s Slew Rate
- 70 ns Settling Time to 0.01%

#### Low Power:

- 7.5 mA Max Power Supply Current Per Amp

#### Easy to Use:

- Drives Unlimited Capacitive Loads
- 50 mA Min Output Current Per Amplifier
- Specified for +5 V,  $\pm 5$  V and  $\pm 15$  V Operation
- 2.0 V p-p Output Swing into a 150  $\Omega$  Load ( $V_S = +5$  V)

#### Good Video Performance

- Differential Gain & Phase Error of 0.07% & 0.11°

#### Excellent DC Performance:

- 2.0 mV Max Input Offset Voltage

### APPLICATIONS

- Unity Gain ADC/DAC Buffer
- Cable Drivers
- 8- and 10-Bit Data Acquisition Systems
- Video Line Driver
- Active Filters

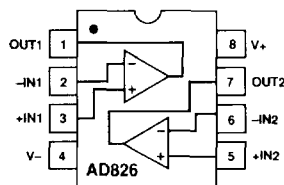
### PRODUCT DESCRIPTION

The AD826 is a dual, high speed voltage feedback op amp. It is ideal for use in applications which require unity gain stability and high output drive capability, such as buffering and cable driving. The 50 MHz bandwidth and 350 V/ $\mu$ s slew rate make the AD826 useful in many high speed applications including: video, CATV, copiers, LCDs, image scanners and fax machines.

The AD826 features high output current drive capability of 50 mA min per amp, and is able to drive unlimited capacitive loads. With a low power supply current of 15 mA max for both amplifiers, the AD826 is a true general purpose operational amplifier.

### CONNECTION DIAGRAM

#### 8-Pin Plastic Mini-DIP and SO Package



The AD826 is ideal for power sensitive applications such as video cameras and portable instrumentation. The AD826 can operate from a single +5 V supply, while still achieving 25 MHz of bandwidth. Furthermore the AD826 is fully specified from a single +5 V to  $\pm 15$  V power supplies.

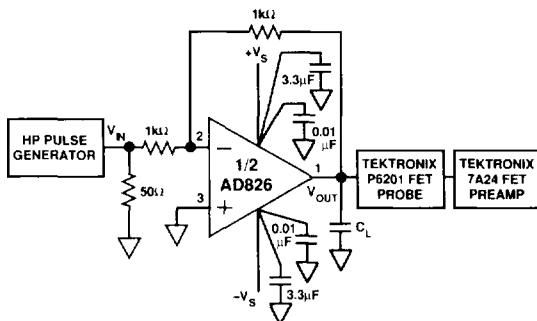
The AD826 excels as an ADC/DAC buffer or active filter in data acquisition systems and achieves a settling time of 70 ns to 0.01%, with a low input offset voltage of 2 mV max. The AD826 is available in small 8-pin plastic mini-DIP and SO packages.

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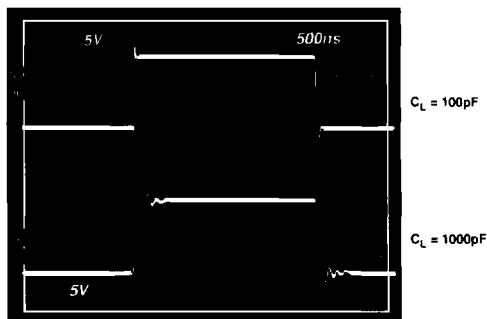
### ORDERING GUIDE

Model	Temperature Range	Package Description	Package Option*
AD826AN	40° C to +85° C	8-Pin Plastic DIP	N-8
AD826AR	40° C to +85° C	8-Pin Plastic SOIC	R-8
AD826AR-REEL	40° C to +85° C	8-Pin Plastic SOIC	R-8

\*For outline information see Package Information section.



Driving a Large Capacitive Load



To obtain the most recent version or complete data sheet, call our fax retrieval system at 1-800-446-6212 or visit our World Wide Web site at <http://www.analog.com>.

# AD826—SPECIFICATIONS (@ $T_A = +25^\circ\text{C}$ , unless otherwise noted)

Parameter	Conditions	$V_S$	Min	Typ	Max	Units
<b>DYNAMIC PERFORMANCE</b>						
Unity Gain Bandwidth		$\pm 5\text{ V}$	30	35		MHz
		$\pm 15\text{ V}$	45	50		MHz
		0, +5 V	25	29		MHz
Bandwidth for 0.1 dB Flatness	Gain = +1	$\pm 5\text{ V}$	10	20		MHz
		$\pm 15\text{ V}$	25	55		MHz
		0, +5 V	10	20		MHz
Full Power Bandwidth	$V_{OUT} = 5\text{ V p-p}$ , $R_{LOAD} = 500\ \Omega$	$\pm 5\text{ V}$		15.9		MHz
	$V_{OUT} = 20\text{ V p-p}$ , $R_{LOAD} = 1\text{ k}\Omega$	$\pm 15\text{ V}$		5.6		MHz
Slew Rate	$R_{LOAD} = 1\text{ k}\Omega$	$\pm 5\text{ V}$	200	250		V/ $\mu\text{s}$
	Gain = 1	$\pm 15\text{ V}$	300	350		V/ $\mu\text{s}$
		0, +5 V	150	200		V/ $\mu\text{s}$
Settling Time to 0.1% to 0.01%	-2.5 V to +2.5 V	$\pm 5\text{ V}$ , $\pm 15\text{ V}$		45		ns
	2.5 V to +2.5 V	$\pm 5\text{ V}$ , $\pm 15\text{ V}$		70		ns
<b>NOISE/HARMONIC PERFORMANCE</b>						
Total Harmonic Distortion	$f_i = 1\text{ MHz}$	$\pm 15\text{ V}$		78		dB
Input Voltage Noise	$f = 10\text{ kHz}$	$\pm 5\text{ V}$ , $\pm 15\text{ V}$		15		nV/ $\sqrt{\text{Hz}}$
Input Current Noise	$f = 10\text{ kHz}$	$\pm 5\text{ V}$ , $\pm 15\text{ V}$		1.5		pA/ $\sqrt{\text{Hz}}$
Differential Gain Error ( $R_I = 150\ \Omega$ )	NTSC Gain = +2	$\pm 15\text{ V}$		0.07	0.1	%
		$\pm 5\text{ V}$		0.12	0.15	%
		0, +5 V		0.15		%
Differential Phase Error ( $R_I = 150\ \Omega$ )	NTSC Gain = +2	$\pm 15\text{ V}$		0.11	0.15	Degrees
		$\pm 5\text{ V}$		0.12	0.15	Degrees
		0, +5 V		0.15		Degrees
<b>DC PERFORMANCE</b>						
Input Offset Voltage		$\pm 5\text{ V}$ to $\pm 15\text{ V}$		0.5	2	mV
Offset Drift				10		$\mu\text{V}/^\circ\text{C}$
Input Bias Current		$\pm 5\text{ V}$ , $\pm 15\text{ V}$		3.3	6.6	$\mu\text{A}$
Input Offset Current		$\pm 5\text{ V}$ , $\pm 15\text{ V}$		25	200	nA
Offset Current Drift				0.3		nA/ $^\circ\text{C}$
Open-Loop Gain	$V_{OUT} = \pm 2.5\text{ V}$ $R_{LOAD} = 500\ \Omega$ $R_{LOAD} = 150\ \Omega$ $V_{OUT} = \pm 10\text{ V}$ $R_{LOAD} = 1\text{ k}\Omega$ $V_{OUT} = +7.5\text{ V}$ $R_{LOAD} = 150\ \Omega$ (50 mA Output)	$\pm 5\text{ V}$	2	4		V/mV
		$\pm 15\text{ V}$	1.5	3		V/mV
		$\pm 15\text{ V}$	3.5	6		V/mV
		$\pm 15\text{ V}$	2	4		V/mV
<b>INPUT CHARACTERISTICS</b>						
Input Resistance				300		k $\Omega$
Input Capacitance				1.5		pF
Input Common-Mode Voltage Range		$\pm 5\text{ V}$	+3.8	+4.3		V
		$\pm 15\text{ V}$	2.7	3.4		V
		0, +5 V	+13	+14.3		V
			12	13.4		V
			+3.8	+4.3		V
			+1.2	+0.9		V
Common-Mode Rejection Ratio	$V_{CM} = \pm 2.5\text{ V}$ , $T_{MIN}$ – $T_{MAX}$ $V_{CM} = +12\text{ V}$	+5 V	80	100		dB
		$\pm 15\text{ V}$	86	120		dB
<b>OUTPUT CHARACTERISTICS</b>						
Output Voltage Swing	$R_{LOAD} = 500\ \Omega$ $R_{LOAD} = 150\ \Omega$ $R_{LOAD} = 1\text{ k}\Omega$ $R_{LOAD} = 500\ \Omega$ $R_{LOAD} = 500\ \Omega$	$\pm 5\text{ V}$	3.3	3.8		$\pm\text{V}$
		$\pm 5\text{ V}$	3.2	3.6		$\pm\text{V}$
		+15 V	13.3	13.7		$\pm\text{V}$
		$\pm 15\text{ V}$	12.8	13.4		$\pm\text{V}$
		0, +5 V	+1.5,			V
			+3.5			V
Output Current		$\pm 15\text{ V}$	50			mA
		$\pm 5\text{ V}$	50			mA
		0, +5 V	30			mA
Short-Circuit Current		$\pm 15\text{ V}$		90		mA
Output Resistance	Open Loop			8		$\Omega$
<b>POWER SUPPLY</b>						
Operating Range	Dual Supply Single Supply		$\pm 2.5$ +5		$\pm 18$ +36	V
Quiescent Current/Amplifier		$\pm 5\text{ V}$		6.6	7.5	mA
Power Supply Rejection Ratio	$V_S = \pm 5\text{ V}$ to $\pm 15\text{ V}$ , $T_{MIN}$ to $T_{MAX}$		75	86		dB

Specifications subject to change without notice.